

S/N 09/726,629

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ajit V. Sathe et al.

Examiner: Xuan M. Thai

Serial No.: 09/726,629

Group Art Unit: 2181

Filed: November 30, 2000

Docket No.: 884.341US1

Title: SOLDERLESS ELECTRONICS PACKAGING (as amended)

Assignee: Intel Corporation

Customer-No: 21186

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AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

SEP 30 2003

Commissioner for Patents

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OFFICIAL

Applicants have reviewed the Office Action mailed on August 27, 2003. Please amend the above-identified patent application as follows.

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Page 2

Dkt: 884,341 US1 (INTEL)

IN THE TITLE

Please amend the title as follows: **SOLDERLESS ELECTRONICS PACKAGING
AND METHODS OF MANUFACTURE**

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FROM: Ann M. McCrackin

OUR REF: 884.341US1

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*** Please deliver to Examiner Xuan Thai in Art Unit 2181. ***

Document(s) Transmitted: Amendment and Response Under 37 CFR 1.111 (20 pages).

Total pages of this transmission, including cover letter: 21 pgs.

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In re. Patent Application of: Ajit V. Sathe et al.

Examiner: Xuan Thai

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Title: SOLDERLESS ELECTRONICS PACKAGING AND METHODS OF
MANUFACTURE

Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

By: Ann M. McCrackin
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I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below.

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